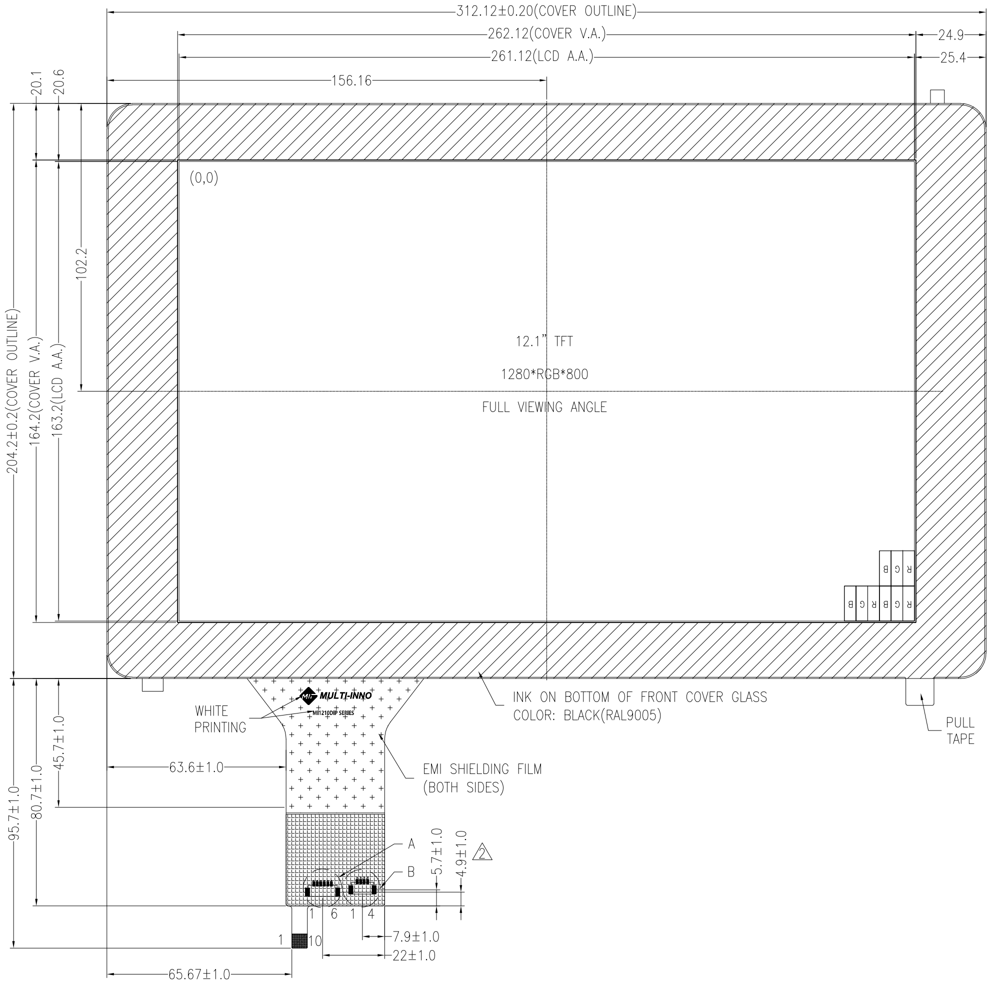
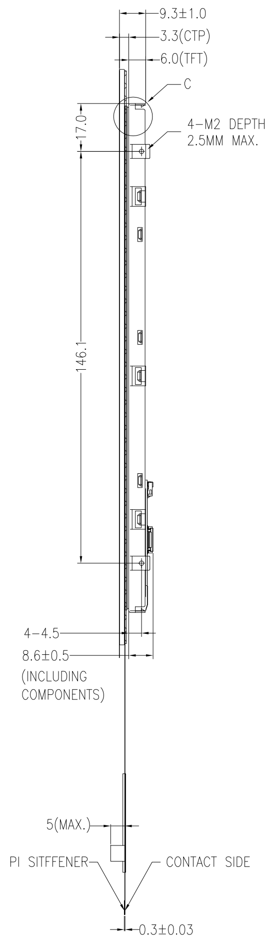
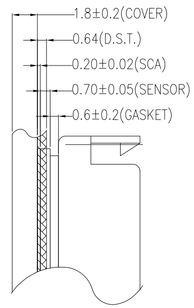
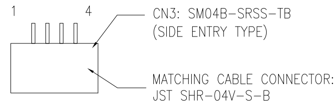
FOR ANY TOUCH PANEL OR OTHER ACCESSORIES ASSEMBLED WITH FRAME GLUE, A PERMANENT AND RELIABLE MECHANICAL STRUCTURE NEEDS TO BE DESIGNED ON CUSTOMER'S WHOLE MACHINE TO HOLD THEM IN PLACE TO AVOID LOOSE.



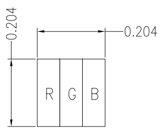




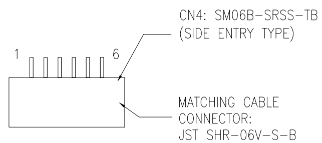
DETAIL C
(SCALE 5:1)



DETAIL A
(SCALE 4:1)



PIXEL DETAIL
(SCALE 100:1)



DETAIL B
(SCALE 4:1)

NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. BACKLIGHT TYPE: WHITE LED, If=240mA, Vf=30.8V~37.4V 2
5. TOP POLARIZER SURFACE TREATMENT: GLARE 3

NOTES FOR OTHERS:

1. OPERATING TEMP: -40℃ ~ 85℃
2. STORAGE TEMP: -40℃ ~ 85℃
3. SURFACE LUMINANCE: 935 CD/M^2 (TYP.)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. UNMARKED TOLERANCE: ±0.5MM
6. ROHS COMPLIANT

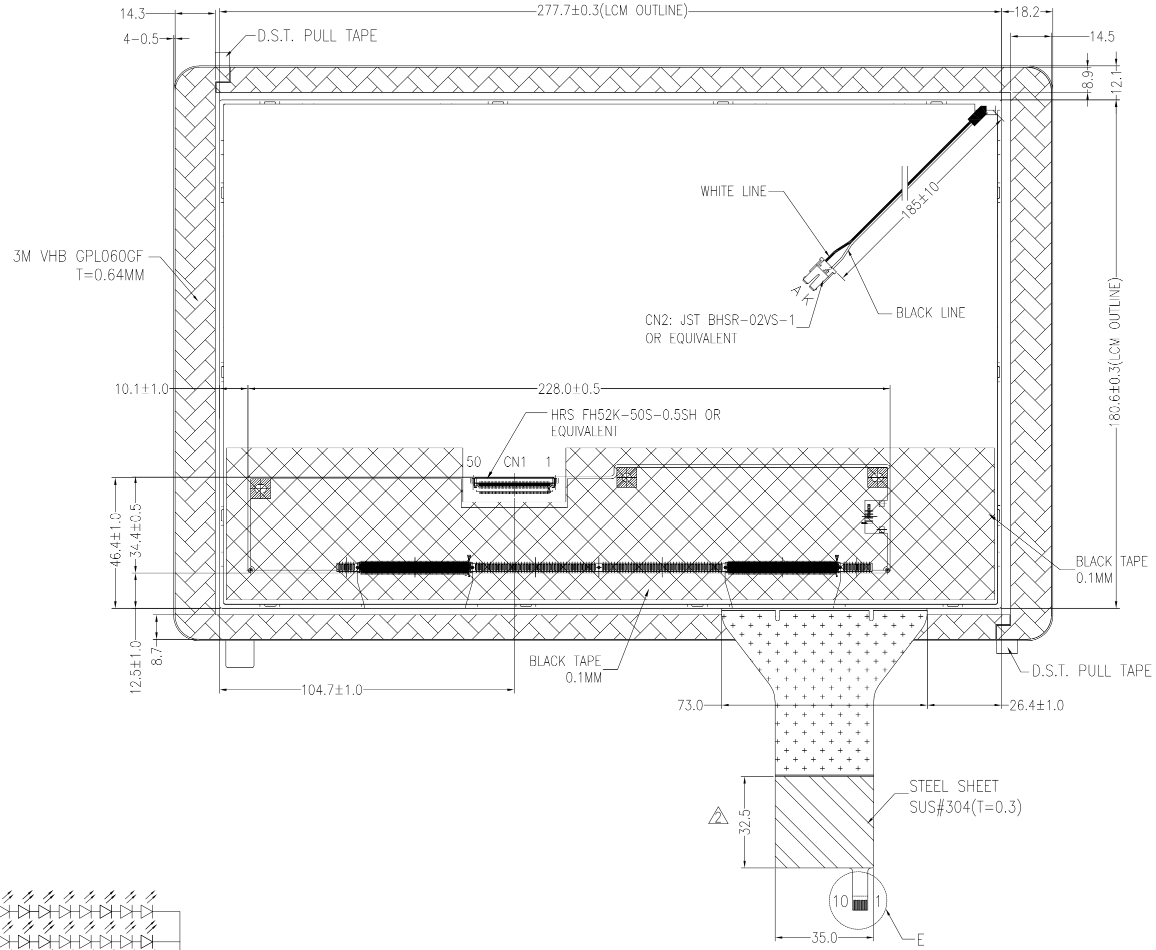
NOTES FOR CTP:

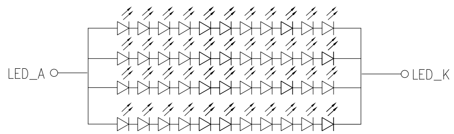
1. 12.1 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: IIC_VDD=3.3V, USB_VDD=5.0V
4. DRIVER IC: ILI2511
5. INTERFACE: IIC /USB
6. RESOLUTION: 9600 x 9600 2
7. MULTI-FINGER: 10
8. HARDNESS OF COVER SURFACE: 7H(750g)
9. FIRMWARE FILE: MI1210DIP-C_FIRMWARE_V25.06.18.00_250618.HEX 2

VER.	REVISED DESCRIPTION	REVISER	DATE
03	CHANGE THE TOP POLARIZER SURFACE TREATMENT	TUFFY	2025.12.05
02	ADD RESOLUTION AND FIRMWARE VERSION CHANGE THE CTP FPC DESIGN CHANGE CURRENT OF BACKLIGHT	RITA	2025.08.06
01	FIRST ISSUE	RITA	2025.04.21

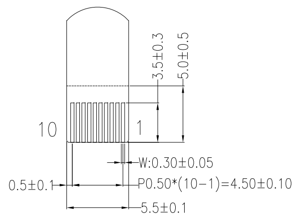
MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA		MODULE P/N: MI1210ACT-1CP1-A
CHECKED BY:		SCALE : 1/1	
APPROVED BY:		UNIT: mm	DESCRIPTION: TFT + CTP MODULE
VERSION NO:	03	SHEET NO: 1/2	





BACKLIGHT CIRCUIT DIAGRAM



DETAIL D
(SCALE 4:1)

CTP (USB & IIC)

PIN	SYMBOL
1	VSS
2	USB_VDD (5.0V)
3	USB_D-
4	USB_D+
5	VSS
6	IIC_VDD (3.3V)
7	IIC_RST
8	IIC_SCL
9	IIC_INT
10	IIC_SDA

CN3 (CTP_USB)

PIN	SYMBOL
1	USB_VDD (5.0V)
2	USB_D-
3	USB_D+
4	VSS

CN4 (CTP_IIC)

PIN	SYMBOL
1	VSS
2	IIC_VDD (3.3V)
3	IIC_RST
4	IIC_SCL
5	IIC_INT
6	IIC_SDA

CN1 (TFT)

PIN	SYMBOL	PIN	SYMBOL
1	NC	26	RX0-
2	NC	27	RX0+
3	NC	28	GND
4	NC	29	RX1-
5	NC	30	RX1+
6	NC	31	GND
7	NC	32	RX2-
8	NC	33	RX2+
9	NC	34	GND
10	NC	35	RXCLK-
11	NC	36	RXCLK+
12	NC	37	GND
13	GND	38	RX3-
14	VPP	39	RX3+
15	NC	40	GND
16	E2PSYSB	41	RES
17	CSB	42	NC
18	SCL	43	STBYB
19	SDO	44	GND
20	SDI	45	SHLR
21	GND	46	NC
22	GND	47	UPDN
23	VCC	48	AB_IND
24	VCC	49	LVBIT
25	GND	50	GND

CN2 (BACKLIGHT)

PIN	SYMBOL
1	A (WHITE LINE)
2	K (BLACK LINE)

VER.	REVISED DESCRIPTION	REVISER	DATE
03	CHANGE THE TOP POLARIZER SURFACE TREATMENT	TUFFY	2025.12.05
02	ADD RESOLUTION AND FIRMWARE VERSION CHANGE THE CTP FPC DESIGN CHANGE CURRENT OF BACKLIGHT	RITA	2025.08.06
01	FIRST ISSUE	RITA	2025.04.21

MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA		MODULE P/N: MI1210ACT-1CP1-A
CHECKED BY:		SCALE : 1/1	
APPROVED BY:		UNIT: mm	DESCRIPTION: TFT + CTP MODULE
VERSION NO:	03	SHEET NO: 2/2	